



0912291628  
Inventor: Yasutaka SAKAINO et al.  
Title of Invention: Semiconductor Integrated  
Circuit  
Attorney Docket No.: 32014-141666  
VENABLE

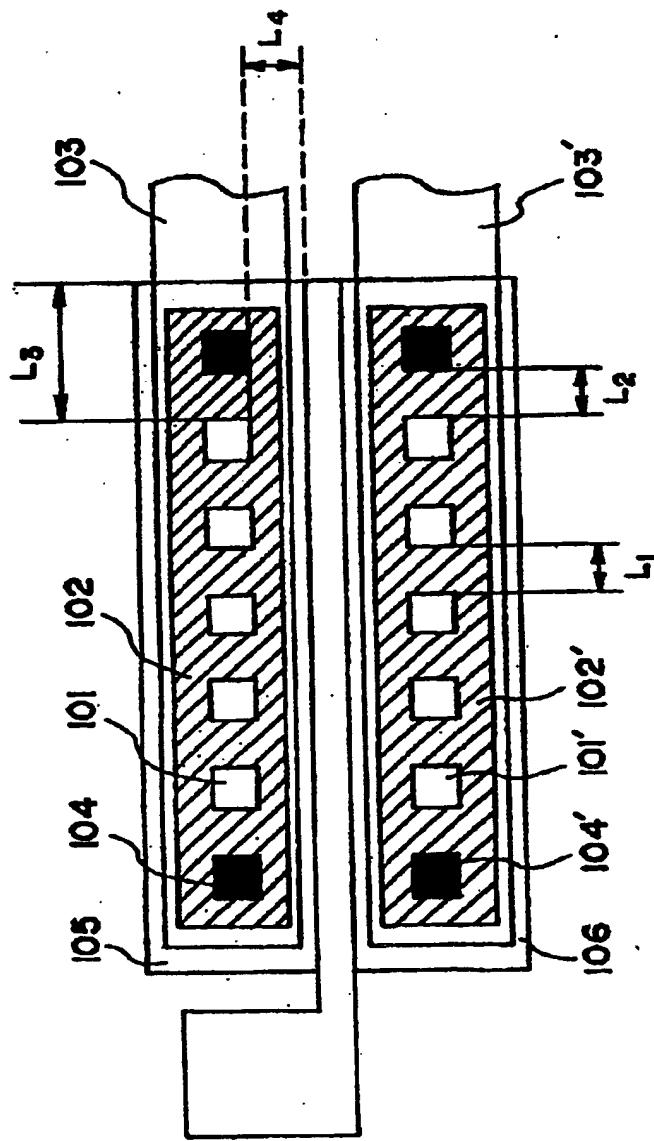
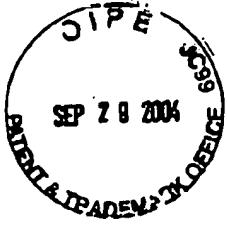


FIG. 1



Inventor: Yasutaka SAKAINO et al.  
Title of Invention: Semiconductor Integrated  
Circuit  
Attorney Docket No.: 32014-141666  
VENABLE

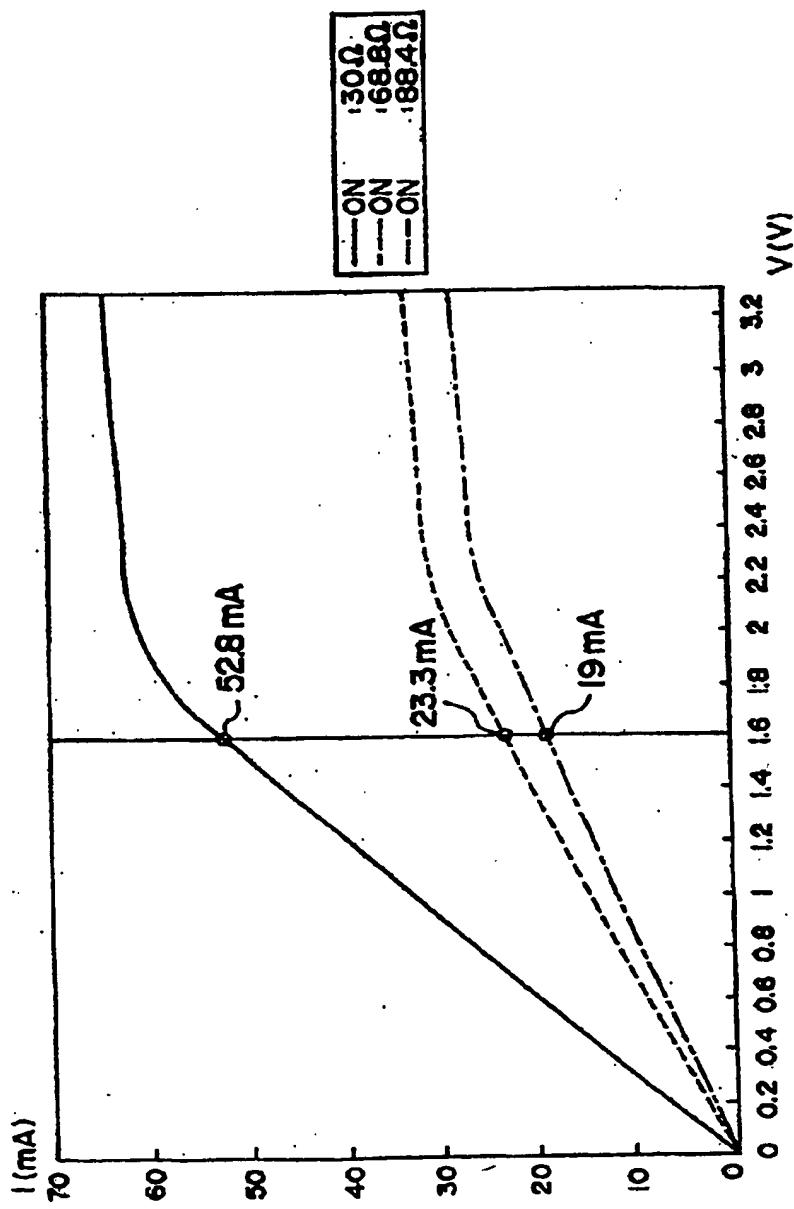
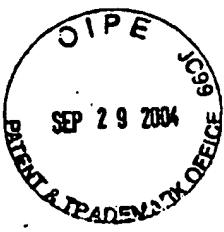


FIG. 2



Inventor: Yasutaka SAKAINO et al.  
Title of Invention: Semiconductor Integrated  
Circuit  
Attorney Docket No.: 32014-141666  
VENABLE

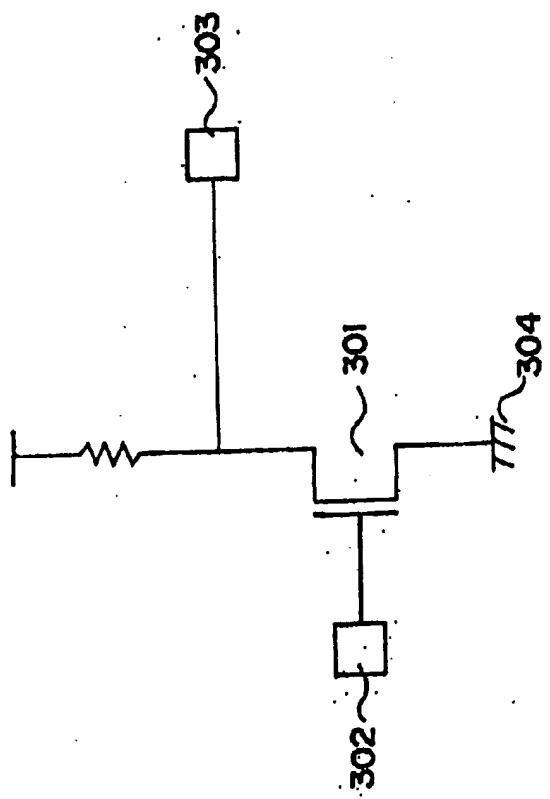
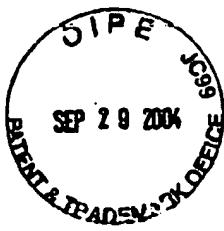


FIG. 3  
PRIOR ART



Inventor: Yasutaka SAKAINO et al.  
Title of Invention: Semiconductor Integrated  
Circuit  
Attorney Docket No.: 32014-141666  
VENABLE

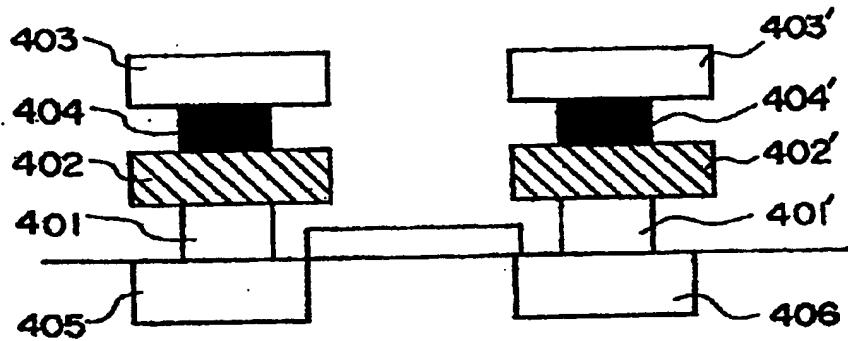


FIG. 4A  
PRIOR ART

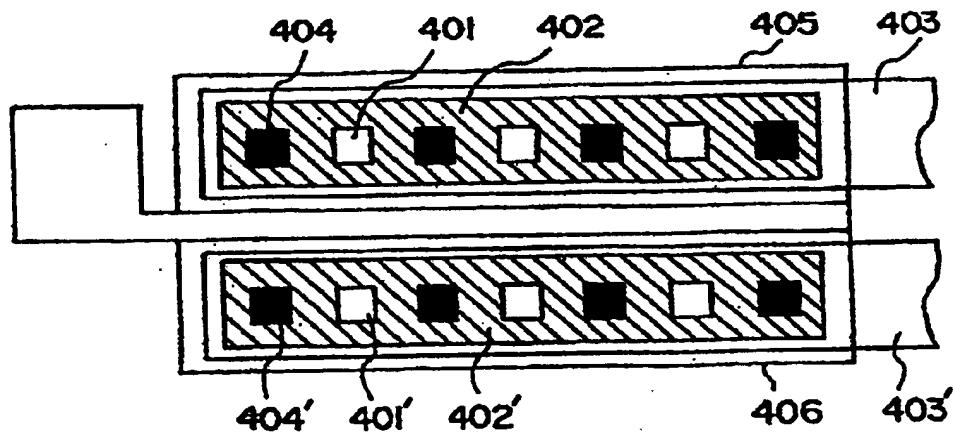


FIG. 4B  
PRIOR ART



Inventor: Yasutaka SAKAINO et al.  
Title of Invention: Semiconductor Integrated  
Circuit  
Attorney Docket No.: 32014-141666  
VENABLE

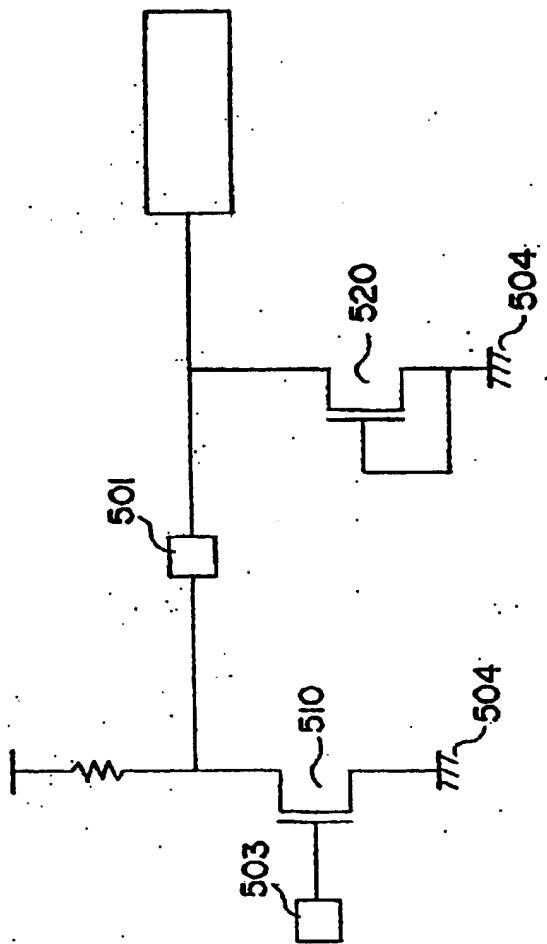


FIG. 5



Inventor: Yasutaka SAKAINO et al.  
Title of Invention: Semiconductor Integrated  
Circuit  
Attorney Docket No.: 32014-141666  
VENABLE

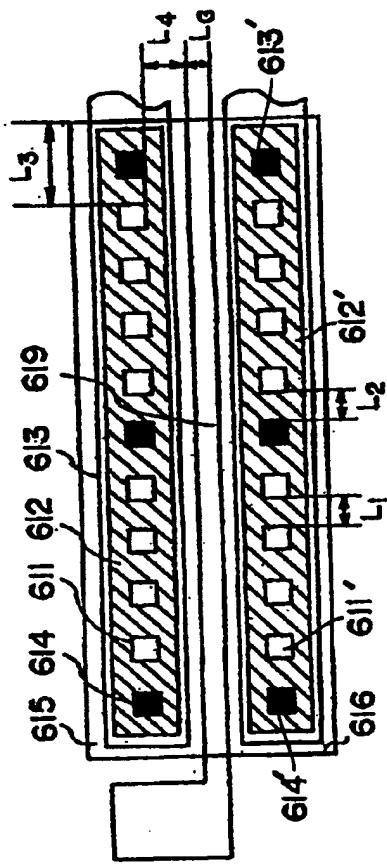


FIG. 6A

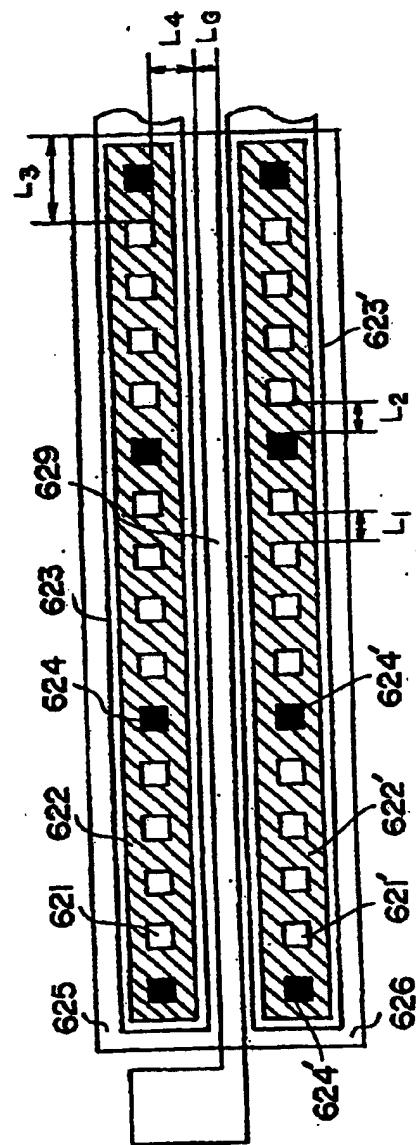


FIG. 6B